

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Takeshi SHIOGA, et al.**

Serial Number: **Not Yet Assigned**

Filed: **July 16, 2003**

For: **SEMICONDUCTOR DEVICE**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 16, 2003

Sir:

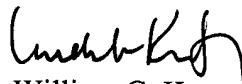
In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

A copy of each of the references are enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 01-2340.

Respectfully submitted,

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PATENT TRADEMARK OFFICE

Enclosures: PTO-1449; References (4)

INFORMATION DISCLOSURE STATEMENT PTO-1449	Atty. Docket No. 030844	Serial No. New Application
	Applicant(s): Takeshi SHIOGA, et al.	
	Filing Date: July 16, 2003	Group Art Unit: Not Yet Assigned

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA	5,404,265	L. Moresco et al.	04/04/95			
_____	AB						
_____	AC						
_____	AD						

FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Translation (Yes or No)
_____	AE	6-224257	08/12/94	Japan	Yes-Abstract/Corresponds to USP 5404265
_____	AF	7-111226	04/25/95	Japan	Yes-Partial and Abstract
_____	AG	2001-102512	04/13/01	Japan	Yes-Partial and Abstract
_____	AH				
_____	AI				

OTHER DOCUMENTS

_____	AJ	
_____	AK	
Examiner		Date Considered